

IMPEDANCE MATCHED LOW NOISE AMPLIFIER

ABSTRACT

[017] The present invention discloses an impedance matched low noise amplifier circuit (10) comprising a serially coupled first resistor (R1) and first transistor (R0), a serially coupled second resistor (R2) and second transistor (R1), a resistive sensor (RMR) coupled to the first transistor (R0) and the second transistor (R1), wherein the first resistor (R1) and the second resistor (R2) are coupled, and a transconductance feedback block (GM) coupled to the resistive sensor (RMR) and to the serially coupled resistors (R1, R2) and transistors (R0, R1).